

Analysis of lumped element transistor structures using MRTD: the equivalent source method

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The Multiresolution Time Domain (MRTD) method is applied to Maxwell's equations and used to simulate lumped element transistor circuits. These lumped element circuits are expressed in terms of equivalent sources, or difference equations derived a priori to describe the behavior of the transistor. In this formulation, the transistor is modeled as a current source truncating the computation region. MRTD results are compared to those generated by LIBRA and good correlation is noted.

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